

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
23 October 2003 (23.10.2003)

PCT

(10) International Publication Number
WO 03/088344 A1

(51) International Patent Classification⁷: **H01L 21/316**,
21/312, C08J 9/26, C09D 183/04, 183/02

(21) International Application Number: PCT/US02/15256

(22) International Filing Date: 10 April 2002 (10.04.2002)

(25) Filing Language: English

(26) Publication Language: English

(71) Applicant (for all designated States except US): **HONEYWELL INTERNATIONAL, INC.** [US/US]; Patent Department, 101 Columbia Road, Morristown, NJ 07962-2245 (US).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **LEUNG, Roger, Y.** [US/US]; 1351 Rimrock Drive, San Jose, CA 95120 (US). **DENG, Eric** [CN/US]; 33288 Jamie Circle, Fremont, CA 94555 (US). **XIE, Songyuan** [CN/US]; 7326 Carter Avenue, Newark, CA 94560 (US). **LU, Victor, Y.** [US/US]; 115 Peach Terrace, Santa Cruz, CA 95060 (US).

(74) Agent: **ROBERTS, Richard, S.**; Roberts & Mercanti, LLP, P.O. Box 484, Princeton, NJ 08542-0484 (US).

(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.

(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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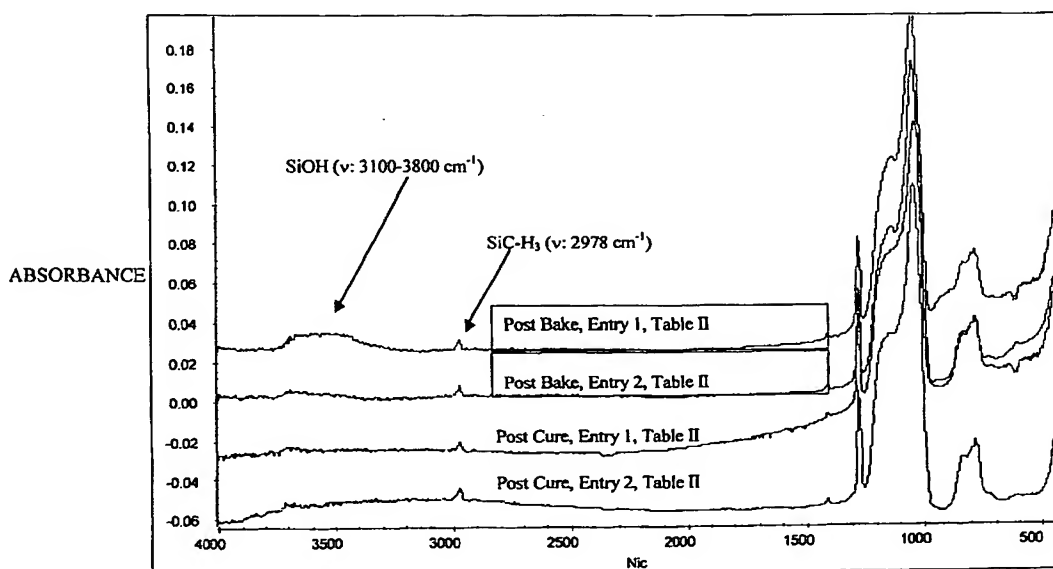
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(54) Title: **LOW METAL POROUS SILICA DIELECTRIC FOR INTEGRAL CIRCUIT APPLICATIONS**



(57) Abstract: The invention relates to the production of nanoporous silica dielectric films and to semiconductor devices and integrated circuits comprising these improved films. The nanoporous films of the invention are prepared using silicon containing pre-polymers and are prepared by a process that allows crosslinking at lowered gel temperatures by means of a metal-ion-free onium or nucleophile catalyst.

507,410



WO 03/088344 A1

INTERNATIONAL SEARCH REPORT

International Application No

P 02/15256

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H01L21/316 H01L21/312 C08J9/26 C09D183/04 C09D183/02

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L C08J C09D

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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X	WO 00 13220 A (ALLIED SIGNAL INC) 9 March 2000 (2000-03-09) claims 1-26 page 9, line 13 - line 24 page 11, line 21 -page 12, line 24 --- -/--	1,31,34, 47,53



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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Date of the actual completion of the international search

12 December 2002

Date of mailing of the international search report

19/12/2002

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Depijper, R

INTERNATIONAL SEARCH REPORT

International Application No.

PCT/US 02/15256

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